Digital Design (2024A)

© Carmi Merimovich

The Academic College of Tel-Aviv

January 29, 2025

Staff

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Teaching assistant	Adi Marinov	adimarinov87@gmail.com	
Excersicers			

Office hour by email

Grading

- Final exam 100% of grade
- Pass if grade ≥ 60
- Exercises
  - Every one/two weeks
  - 100% Mandatory submission

Course Objectives

Gate Level Computer Structure

- Number representations
- Boolean algebra
- Gates
- Combinational circuits
- Sequential circuits

Course Update

In **form**, not in **content**

- There are new beautiful yellowish slides
- Verilog is used to describe circuits
- Icarus Verilog is used for circuit simulation

Most importantly

- There is a teaching assistant

Books

- M. Morris Mano, *Digita Design*, Pearson
- Brock J. LaMers, *Introduction to Logic Circuits & Logic Design with Verilog*, Springer
- Davis Harris and Sarah Harris, *Digital Design and Computer Architecture*, Morgan Kaufman

Really

Most books with title words 'Digital Design' or 'Logic Circuits' and 'Verilog' will do

Slides

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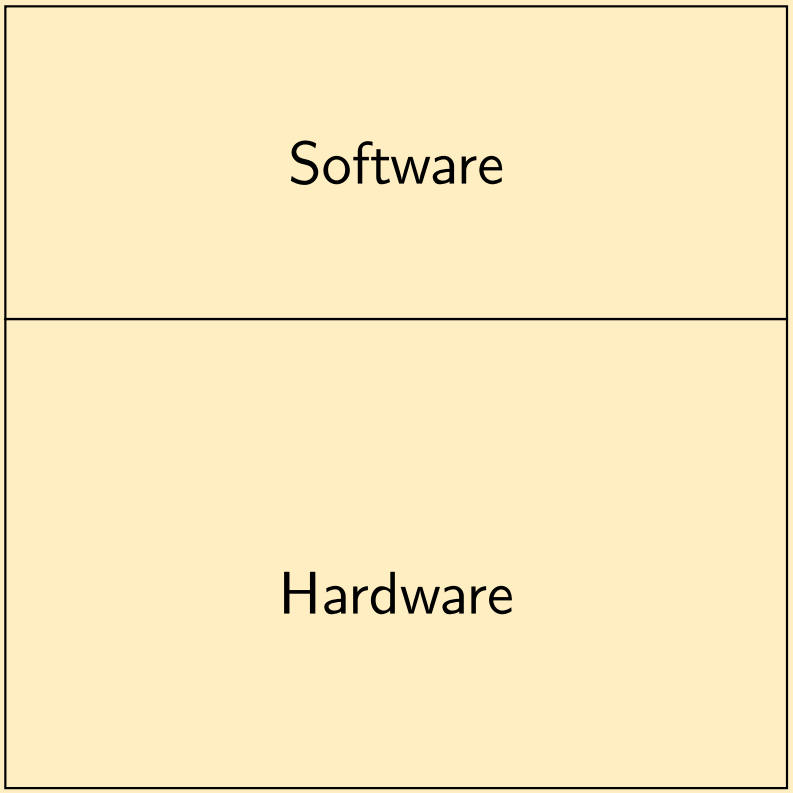
- You probably found the slides here
- We study topics 1 through 7 inclusive
- Topics 6 and 7 are still in flux
- The slides will probably be modified along the semester
  - (There are always glitches with new slides)

Where Do We Stand?



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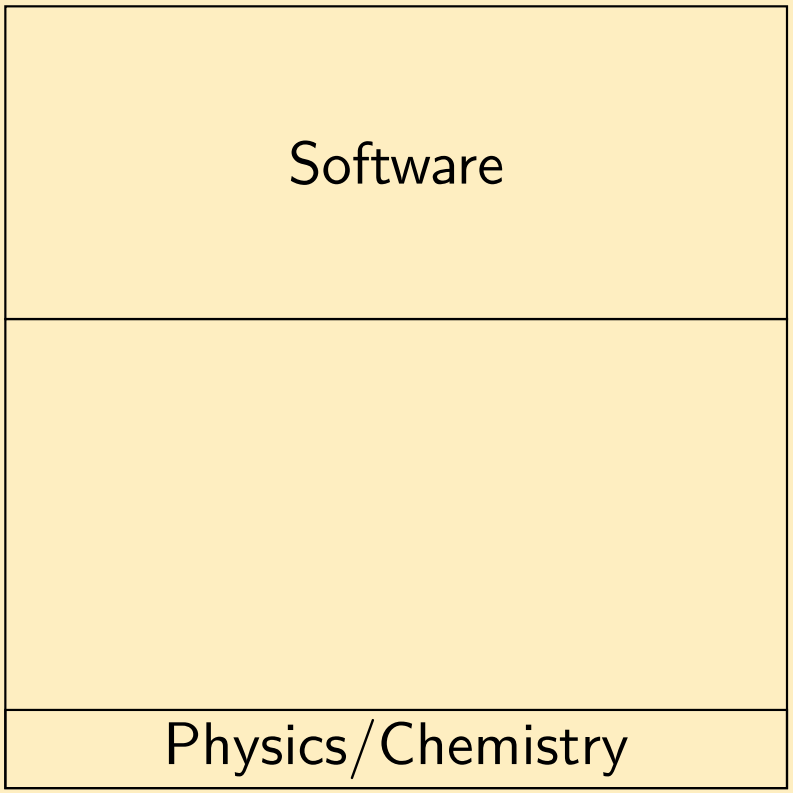

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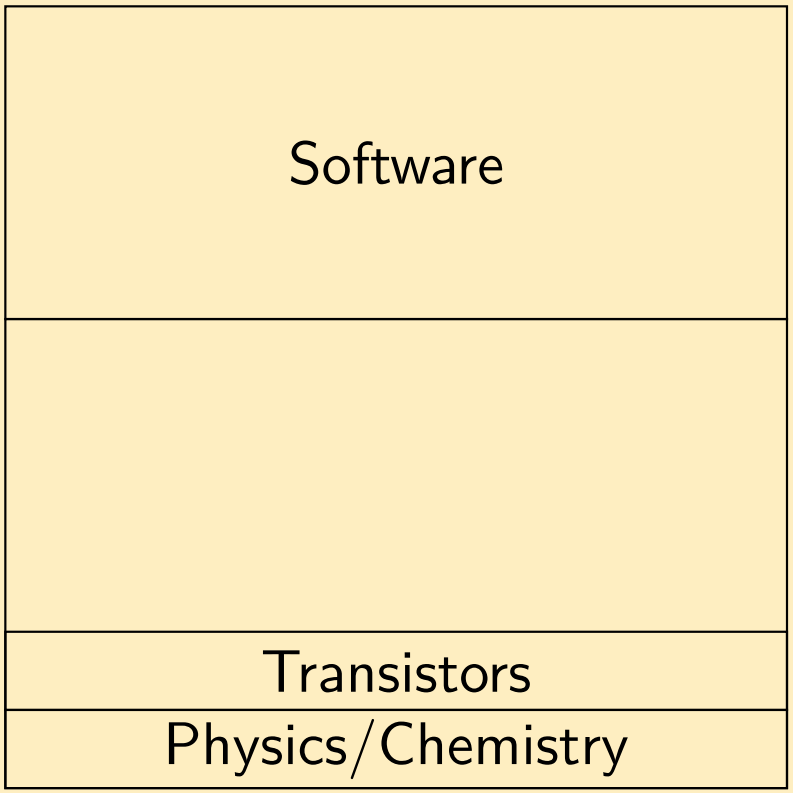

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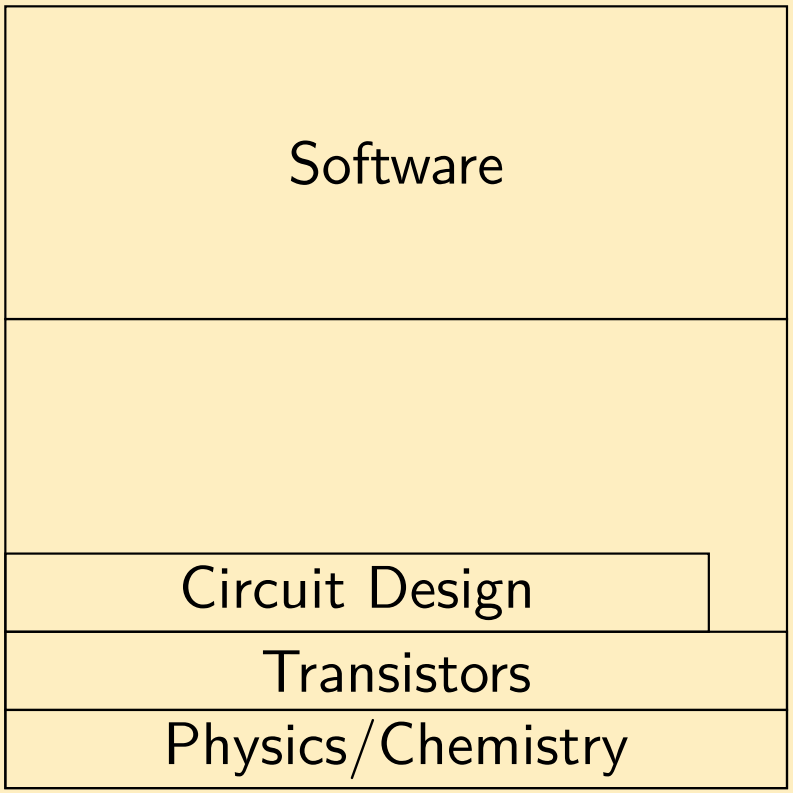

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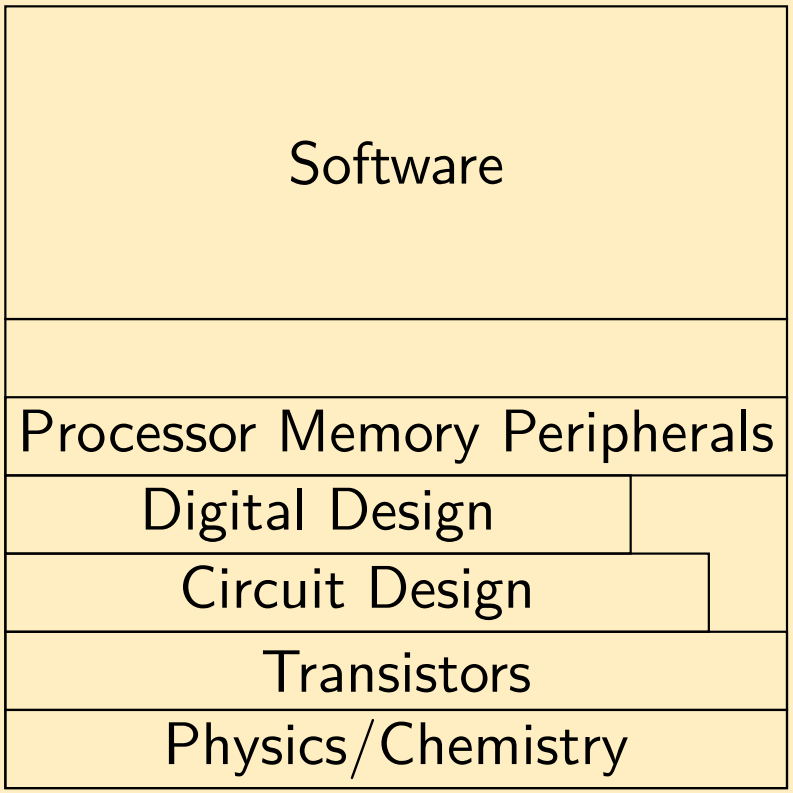

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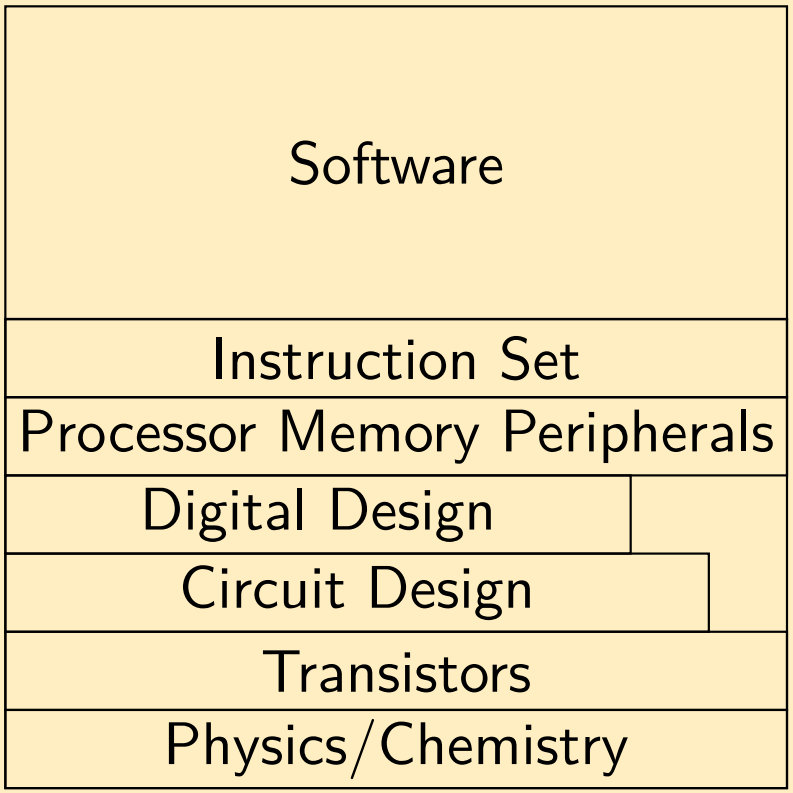

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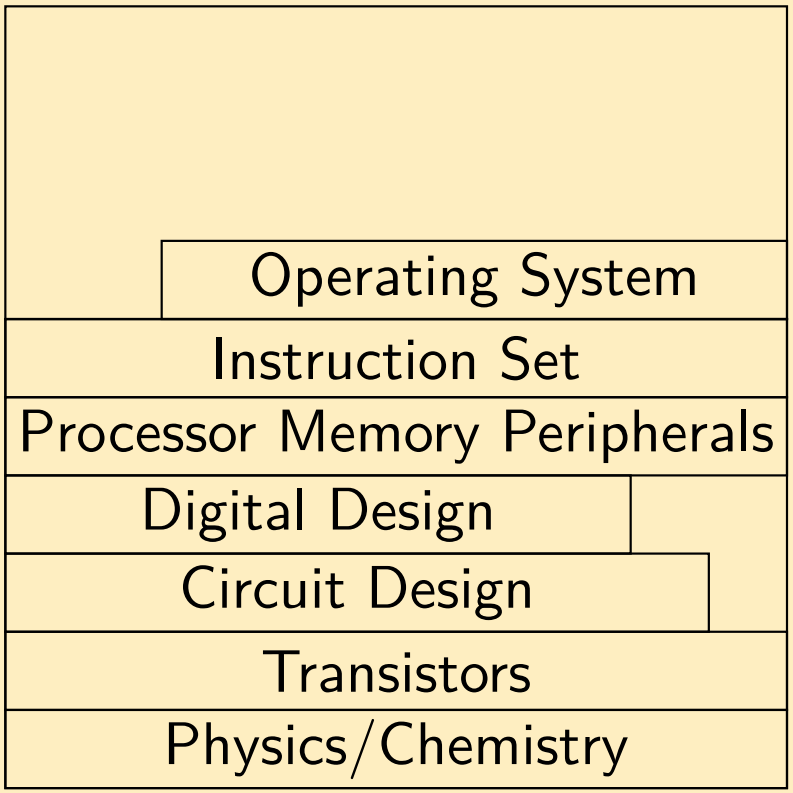

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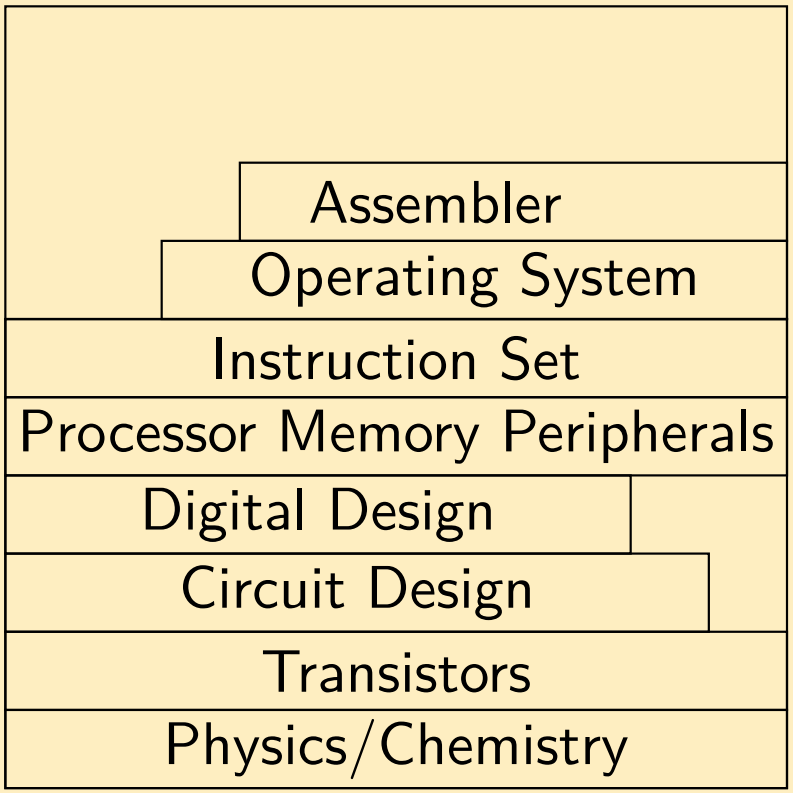

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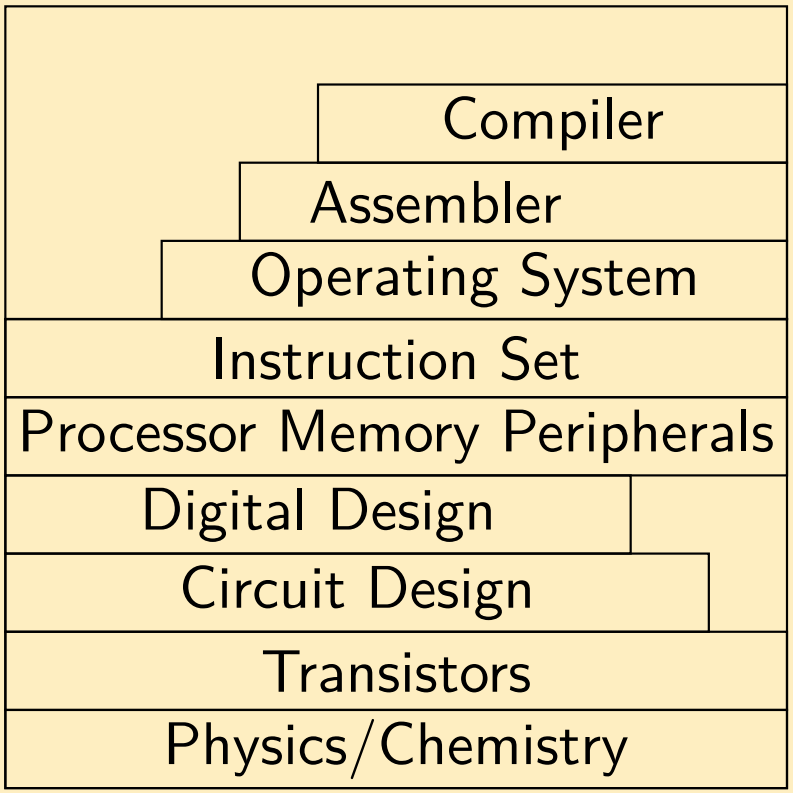

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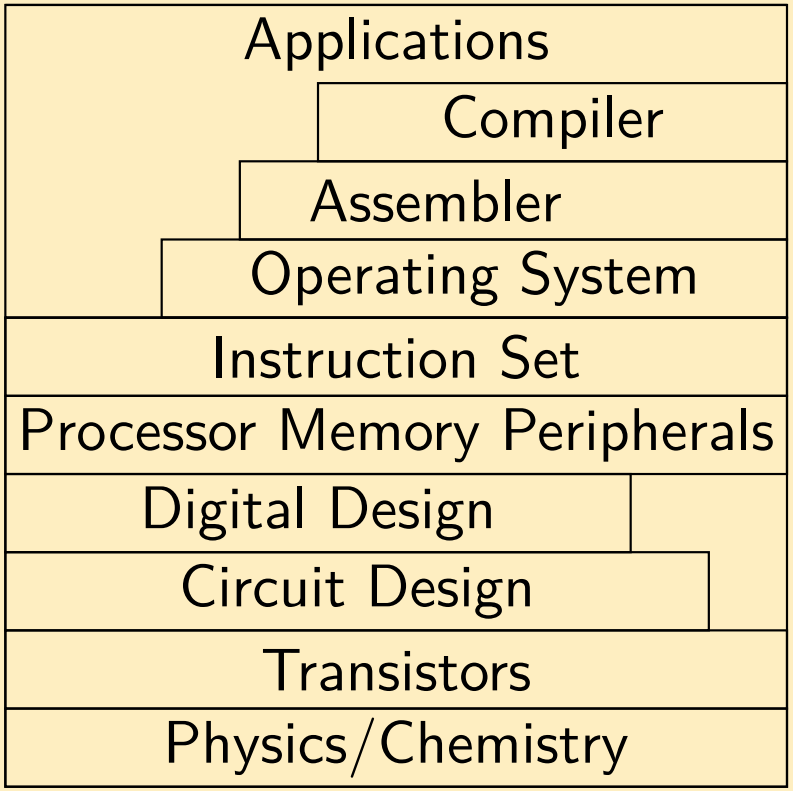

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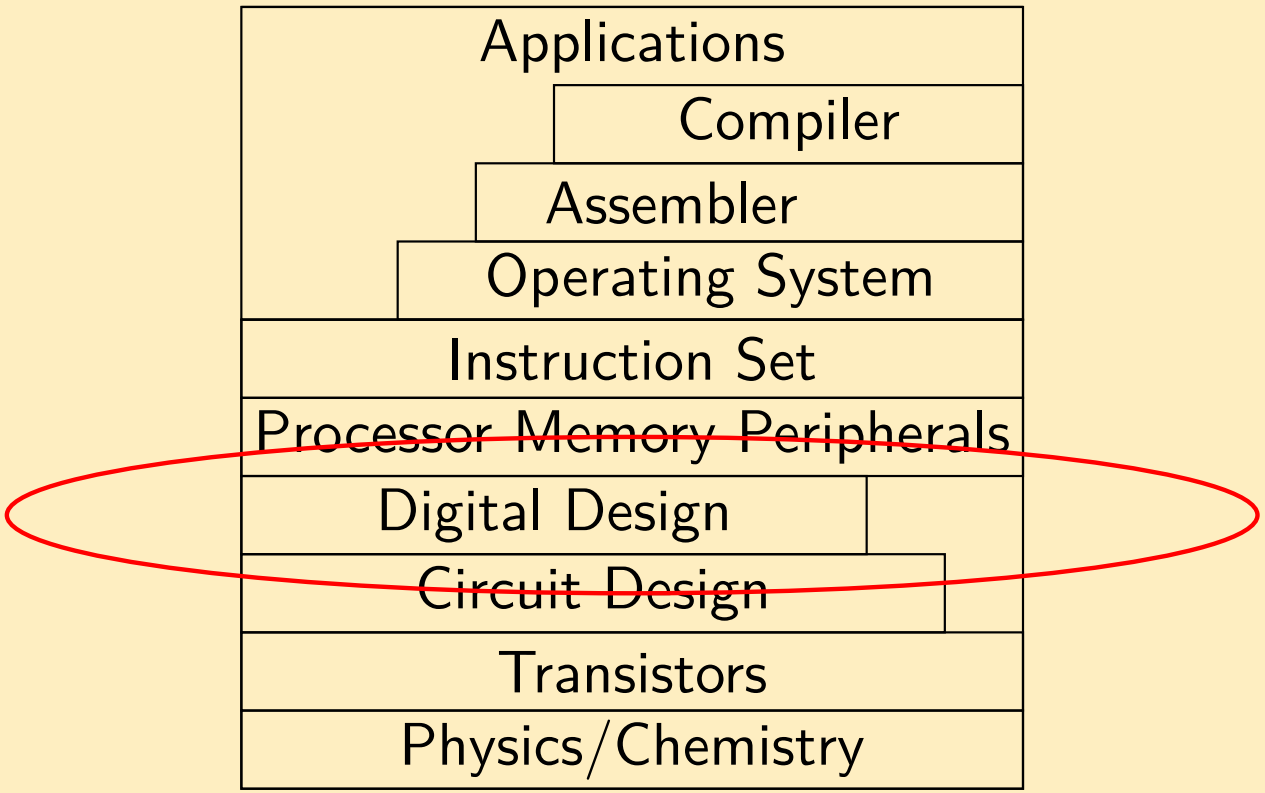

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